

ABSTRACT OF THE DISCLOSURE

A signal processing circuit generating a digital signal based on an input pulse signal is provided, which circuit includes a clock pulse output circuit which
5 outputs clock pulses having one of positive and negative polarities for a period which includes a pulse of the input pulse signal, a counter circuit which counts the clock pulses, and an output circuit which outputs the digital signal based on a counted value of the counter
10 circuit.

0974066, 121900